

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S108	21255	((codec coder decoder) near3 (switch\$3 chang\$3) )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 10:19
S109	3068	((codec coder decoder) near3 (switch\$3 chang\$3) ) with (produc\$3 compute computing computed calculat\$3 generat\$3 )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 10:23
S110	908	380/44.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 10:21
S111	3	S109 and S110	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 10:21
S112	31	((codec coder decoder) near3 (switch\$3 chang\$3) ) with (produc\$3 compute computing computed calculat\$3 generat\$3 deriv\$3) near2 key	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 11:03
S113	42	((codec coder decoder) near3 (switch\$3 chang\$3) ) with (produc\$3 compute computing computed calculat\$3 generat\$3 deriv\$3) near3 key	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 10:24
S114	11	S113 not S112	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 10:34
S115	45	((codec encoder coder decoder) near3 (switch\$3 chang\$3) ) with (produc\$3 compute computing computed calculat\$3 generat\$3 deriv\$3) near2 key	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 11:16
S116	56	((codec encoder coder decoder) near3 (switch\$3 chang\$3) ) with (produc\$3 compute computing computed calculat\$3 generat\$3 deriv\$3 new ) near2 key	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 11:30

## EAST Search History

S11 7	24	S116 not S113	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 11:45
S11 8	3	voice near packet with encrypt\$3 same ((new second ) near2 key rekey\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 11:46
S11 9	381	packet with encrypt\$3 same ((new second ) near2 key rekey\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 11:47
S12 0	86	packet with encrypt\$3 same ((new second ) near2 key rekey\$3) and (codec encoder coder decoder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/30 11:47

Searching for **PHRASE change key codec encoder decoder switch**.

Restrict to: [Header](#) [Title](#) Order by: [Expected citations](#) [Hubs](#) [Usage](#) [Date](#) Try: [Google \(CiteSeer\)](#)  
[Google \(Web\)](#) [Yahoo!](#) [MSN](#) [CSB](#) [DBLP](#)

**No documents match Boolean query. Trying non-Boolean relevance query.**

500 documents found. **Only retrieving 125 documents (System busy - maximum reduced). Order: relevance to query.**

[Efficient realizations of encoders and decoders based on... - Chakrabarti, Mumford \(1998\)](#) (Correct) (11 citations)

one that is best suited for a specific application. **Keywords**-Discrete Wavelet Transform architectures,  
 1 Efficient realizations of **encoders** and **decoders** based on the 2-D Discrete Wavelet  
 1 Efficient realizations of **encoders** and **decoders** based on the 2-D Discrete Wavelet Transform  
[enws155.eas.asu.edu:8001/jourpapers/2D.ps](#)

[Power Optimization of System-Level Address Buses.. - Fornaciari.. \(2000\)](#) (Correct) (8 citations)

has been set up for the design of high-performance **encoder /decoder** architectures to reduce the transition  
 set up for the design of high-performance **encoder /decoder** architectures to reduce the transition activity  
 defining a methodology for the optimization of the **switching** power related to the processor-to memory  
[www.sigda.org/Archives/ProceedingArchives/Codes/Codes2000/papers/2000/codes00/htmfiles/SUN\\_SGI/.../pdf](#)

[Control of LQG Systems Under Communication Constraints - Tatikonda, Sahai, Mitter \(1998\)](#) (Correct) (8 citations)

[web.mit.edu/afs/athena.mit.edu/user/s/a/sahai/www/Research/acc1999.ps](#)

[Simple dynamically reconfigurable OCDMA.. - Mokhtar, Ibsen, Teh, ... \(Correct\)](#)

center wavelength due to strain and temperature **changes** is given by [8]  $T \cdot n \cdot T \cdot n \cdot l \cdot n \cdot l \cdot n \cdot e \cdot f \cdot f$   
 Simple dynamically reconfigurable OCDMA **encoder/decoder** based on a uniform fiber Bragg grating  
 Simple dynamically reconfigurable OCDMA **encoder/decoder** based on a uniform fiber Bragg grating M. R.  
[www.southamptonphotonics.com/\\_downloads/tech\\_papers/reconfig\\_OCDMA.PDF](#)

[TCM on Frequency-Selective Land-Mobile Fading Channels - Hoeher \(1991\)](#) (Correct) (10 citations)

for stationary and mobile users. 2.2.1 Source **Encoder/Decoder** The source **encoder** transforms the  
 even delivers reliability information to an outer **decoder**. The outer code is a rate-compatible punctured  
 and the subframe length is almost arbitrary (**switching** is possible after multiples of the (short)  
[www-Ins.tf.uni-kiel.de/ict/download/tirrenia.ps.gz](#)

[Long Distance Differential Transmission of DS Links over... - Haas, Liu, MARTIN \(Correct\)](#) (1 citation)

[2]The differential resistance of the diodes **changes** with the input voltage, so the termination  
 the typical output of the driver. A list of the **key** features of the AT&T buffer is given below :  
 consists of four functional blocks :the NRZ-to-DS **encoder/decoder**, the ECL-TTL leveltranslators and the  
[wotug.ukc.ac.uk/parallel/vendors/inmos/ieee-hic/copper.ps.gz](#)

[An Approach to Vector Quantization over Binary Gaussian Channels ... - Skoglund \(Correct\)](#)

is introduced into the channel input data. The **key** being to allow transmission errors to occur but  
 general codebooks. We also treat the problem of **encoder** optimization, and combined source-channel  
 We focus on the decoding problem, and present **decoders** that base decoding on soft estimates of the  
[ftp.s3.kth.se/pub/signal/reports/98/IR-S3-SB-9811.ps](#)

[Multirate Schemes and Multiuser Decoding in DS/CDMA Systems - Ottosson \(1995\)](#) (Correct)

a constant processing gain, is to let the bit rate **change** the chip-rate [75]Hence a multi chip-rate  
**decoders** and the detectors, are significant. **Keywords**: Direct-Sequence Code Division  
 images, depending on the type of speech and image **encoders** used in the system, to or lower for data  
[www.s2.chalmers.se/~tonyo/publications/papers/TR\\_214L.ps.gz](#)

[A Vlsi Architecture For Real-Time Hierarchical.. - Mohan Vishwanath.. \(1994\)](#) (Correct) (1 citation)

the transform coding part of the architecture, the **change** is minimal. Recall from the previous section,

In Part By A Grant From Nsf, No. Mip-9309504. **Codec Codec Spa Ispa To Network Or Transcoder Send** supports single chip implementations of the **encoder**, the **decoder**, and the transcoder for some enws155.eas.asu.edu:8001/confpapers/hierar.ps

Lecture 2 - Spring Chen Each (Correct)

LH, HL, HH H 2 (z) Highpass 2 H 1 (z) Lowpass 2 **CODEC 1 CODEC 2 2 2 F 1 (z) F 2 (z) Lowpass Best Match Codebook Index Table Lookup Codebook Encoder Decoder x k y k y k 18-899/Spring Codebook Index Table Lookup Codebook Encoder Decoder x k y k y k 18-899/Spring 1998/Chen** www.ece.cmu.edu/~ee899/lecture2.ps

Streaming Video With Optimized Reconstruction-Based Dct - Xiao Su And (2000) (Correct)

Q IQ Figure 1: Basic building blocks of a modified **codec**. The shaded block is our proposed ORB-DCT. transmissions over the Internet without Inverse **Encoder Decoder Transform Transform x Y C Z** may worsen network traffic, or rely solely on **decoders** with inadequate error concealment. This paper manip.crhc.uiuc.edu/~wah/Wah/papers/Dir/C130/C130.ps.gz

Reusable Memories In The Light Of The Old Arbitrarily.. - Ahlswede, Simonyi (1990) (Correct)

1's (resp. 0's) in some of n positions, that is **change y n** in those positions to 1 (resp. 0) This is general) memories with side information. If the **encoder** has no side information it is still not two persons (or devices) the **encoder E** and the **decoder D**. They use the memory in so called cycles. In www.mathematik.uni-bielefeld.de/ahlswe/papers/./pub/ahlswe/memories.ps

A Method to Implement a Knowledge-Based System for Fast.. - Chaouat, Vachoux, Mlynek (Correct)

process at all levels of abstraction. As design **changes** are costly in terms of time and money, problems to reuse specifications at the system level. **Keywords** Knowledge-based system, hardware systems ENUM (parallel, pipeline, low\_power) **Encoder**: STRING Syndrome: STRING Input width Output c3iwww.epfl.ch/publications/houston.ps

Design and Implementation of a Teleautonomous Hummer - Bentivegna (1998) (Correct) (1 citation)

which allows a user to move the actuators and **change** the parameters. This program also displays the has been demonstrated in outdoor experiments. **Keywords**: mobile robotics, teleautonomy, feedback unit includes a positioning motor, motor position **encoder**, brake position potentiometer, reduction gears, ftp.cc.gatech.edu/pub/people/arkin/web-papers/hummer.ps.Z

Information Processing By A Perceptron - Nadal, Parga (1992) (Correct)

up to  $p = N$ . Above this value there is a **change** in the behavior of the capacity. In the large N will undergo this operation in what we can call an **encoder**. In this work we describe our results for a www.ft.uam.es/~parga/ARTICLES/CONFERENCES/elba92\_binary.ps.gz

Unit-Memory Hamming Turbo Codes - Cheng, McEliece (1995) (Correct) (4 citations)

potentials than the SI turbo (SIT) codes. 2 **Encoder** Consider the (8 4 3 8) UM Hamming code [5] (2) p y 2) u W 1) V 1) Figure 4: Turbo **decoder** for (3(K 4) 4 K) codes. Combining this and www.systems.caltech.edu/EE/Groups/communications/jfc/papers/ISIT95.umht.ps

A Buffer-triggered Smooth Adaptation Technique for Synchronized .. - Rakow, al. (Correct)

as discrete data and when user interactions like **change** in direction or speed have to be supported. In In [RPSL94] the integration of a software MPEG **decoder** into a "continuous media player" is described. system detect a bottleneck and respond to it by **switching** to lower presentation quality to reduce size www.darmstadt.gmd.de/~edrg9/final/s2p1-rakow.ps.gz

Issues in Goal-Driven Explanation - Leake (1994) (Correct) (1 citation)

knowledge and goals must in turn be able to **change** the strategies (including internal reasoning and explains surprising events for its internal use, a **key** motivation for explaining is to perform learning explanation construction process must be able to **switch** its attention between candidate explanations as www.cs.indiana.edu/hyplan/leake/papers/p-94-01.ps.Z

Design Methodology of Ultra Low-power MPEG4 Codec Core.. - Kimiyoshi Usami.. (1998) (Correct) (3 citations)

as Dual-V DD approaches. These techniques do not **change** the critical path delay, resulting in keeping the the original in three week turn-around-time. 1.1 **Keywords** Low power, voltage scaling, design Design Methodology of Ultra Low-power MPEG4 **Codec Core Exploiting Voltage Scaling Techniques**

herkules.informatik.tu-chemnitz.de/proceedings/dac-98/sun\_sgi/.../pdffiles/29\_1.pdf

An Algorithm for Playout of Packet Voice based on Adaptive.. - Christensen (1999) (Correct) (12 citations)  
they encounter a variable amount of delay that **changes** the deterministic time intervals. A receiving  
to encoding methods in the voice coder/**decoder (codec)** at the sender or **changes** to the existing  
**changes** to encoding methods in the voice coder/**decoder (codec)** at the sender or **changes** to the existing  
www.csee.usf.edu/~christen/lcn99\_1.pdf

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